TOSHIBA Insulated Gate Bipolar Transistor Silicon N Channel IGBT

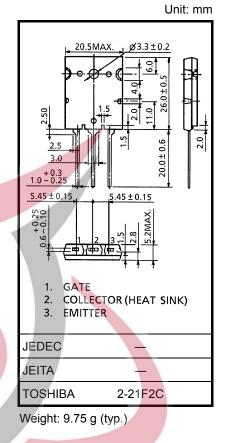
# GT60J323H

Current Resonance Inverter Switching Application Induction Heating Cooking Appliances Induction Heating Appliances

- Enhancement mode type
- High speed :  $t_f = 0.12 \ \mu s$  (typ.) (I<sub>C</sub> = 60A)
- Low saturation voltage: VCE (sat) = 2.1 V (typ.) (IC = 60A)
- FRD included between emitter and collector
- Fourth generation IGBT
- TO-3P(LH) (Toshiba package name)

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Collector-emitter voltage		V <sub>CES</sub>	600	V
Gate-emitter voltage		V <sub>GES</sub>	±25	V
Continuous collector	@ Tc = 100°C	IC	30	Α
current	@ Tc = 25°C	IC.	60	A
Pulsed collector current		ICP	120	А
Diode forward current	DC	١ <sub>F</sub>	30	A
	Puised	I <sub>FP</sub>	120	2
Collector power	@ Tc = 100°C	Pc	68	W
dissipation	@ Tc = 25°C	FC	170	vv
Junction temperature		Tj	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C



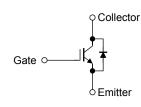
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

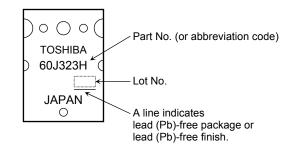
#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit	с Т	R	0	Ν	L	С
Thermal resistance (IGBT)	R <sub>th (j-c)</sub>	0.74	°C/W						
Thermal resistance (diode)	R <sub>th (j-c)</sub>	1.56	°C/W						

### **Equivalent Circuit**



### Marking

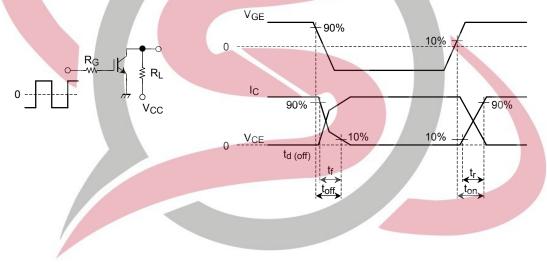


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Electrical Characteristics (Ta = 25°C)

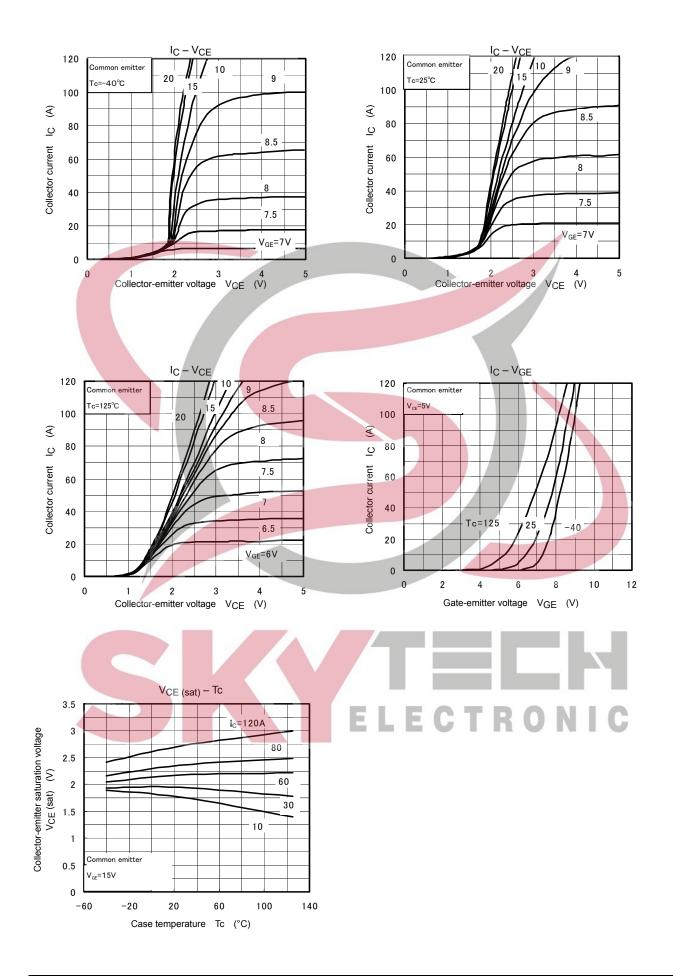
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		IGES	$V_{GE}$ = ±25 V, $V_{CE}$ = 0	—	_	±500	nA	
Collector cut-off current		ICES	V <sub>CE</sub> = 600 V, V <sub>GE</sub> = 0	_	—	1.0	mA	
Gate-emitter cut-off voltage		V <sub>GE (OFF)</sub>	$I_{C}$ = 60 mA, $V_{CE}$ = 5 V	3.0	—	6.0	V	
Collector-emitter saturation voltage		V <sub>CE (sat)</sub>	I <sub>C</sub> = 60 A, V <sub>GE</sub> = 15 V	_	2.1	2.9	V	
Input capacitance		C <sub>ies</sub>	$V_{CE}$ = 10 V, $V_{GE}$ = 0, f = 1 MHz		4800	—	pF	
Switching time	Rise time	t <sub>r</sub>	Resistive Load	_	0.26	_		
	Turn-on time	t <sub>on</sub>	V <sub>CC</sub> = 300 V, I <sub>C</sub> = 60 A	_	0.39	_	μs	
	Fall time	t <sub>f</sub>	$V_{GG}$ = ±15 V, R <sub>G</sub> = 30 $\Omega$	_	0.12	0.21		
	Turn-off time	toff	(Note 1)	-	0.41	_		
Diode forward voltage		VF	I <sub>F</sub> = 30 A, V <sub>GE</sub> = 0	- /	1.4	2.0	V	
Reverse recovery time		t <sub>rr</sub>	I <sub>F</sub> = 30 A, di/dt = −100 A/μs	-	0.1	0.2	μs	

Note 1: Switching time measurement circuit and input/output waveforms



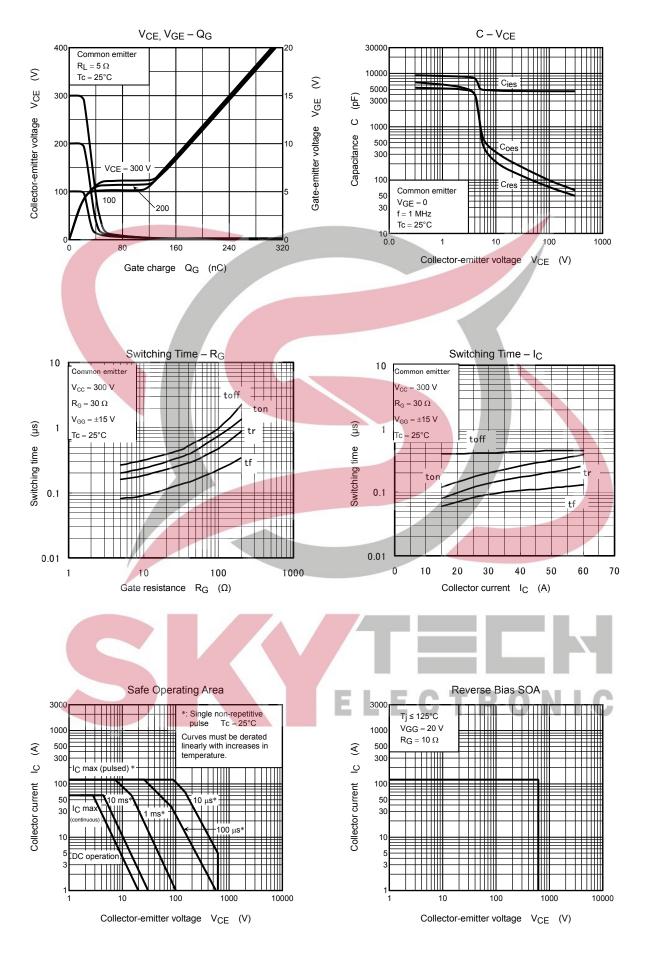


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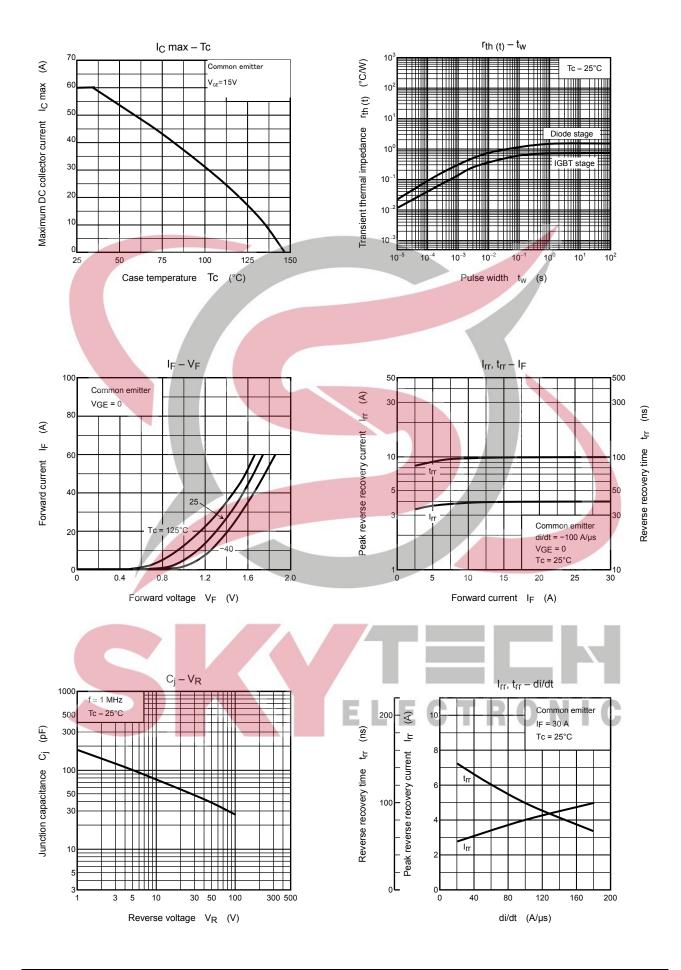
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